Design of Optimum Microstrip to Substrate Integrated Waveguide Transition

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Abstract. The excitation of a Substrate Integrated Waveguide (SIW) makes use of costly and complex structures. In this paper, two simple structures have been presented to validate our computing results. This is a step discontinuity added to the microstrip line to produce an impedance matching between the SIW line and the line. Based on a small approximation of reflection, design equations are deduced, which allowed an optimal dimensioning of the step discontinuity (L×W). This design strategy was tested by simulating several waveguides, and a good agreement between the optimal geometric parameters and the calculated values was obtained.

Keywords: SIW, transition, small reflection approximation.

1 Introduction

Substrate Integrated Waveguide is a new form of the waveguide. It is a mixed technology between the waveguide and printed circuits that benefit from the advantages and high performances of both technologies [1]. This new structure consists of a substrate metalized on up and down sides with two parallel rows of metallic vias inserted in the substrate. If the diameter of the metallic vias and the distance between them are chosen properly, the energy leaking between consecutive vias is negligible, hence, the structure behaves like a classical rectangular waveguide [2]. Because of the advantages of this technology most microwave components in a classical waveguide, such as filters, couplers, antenna and beam former, were modeled in SIW technology [3-7].

To interface a signal between microstrip technology and SIW structure a transition is used. Figure (1) presents the transition structure used in this work to connect a 50 Ω microstrip line to the planar SIW. The integral design is realized on the same substrate. The purpose of the transition is to transform the quasi-TEM mode in the microstrip line to the dominant TE10 mode in the SIW. Since there is an impedance matching between the microstrip line and the SIW structure, return loss will be minimum, for this reason, the length and width of the transition structure has to be optimized.

This paper presents design equations for geometrical parameters (width and length) of the structure. These equations allow, in almost all cases, a direct calculation of the optimum width and length of the transition without a full-wave simulator. This method has been tested by applying it to microstrip to SIW transition with three examples using different substrates at different frequencies.



Fig. 1.Microstrip line to substrate integrated waveguide transition.



Fig. 2. Equivalent electric circuit.

2 Design Procedure

First, based on our previous work [7], a new formula for calculating the optimum width of a SIW, corresponding to the dominant mode, is used in this paper, by applying the Finite Difference Frequency Domain (FDFD) method. Secondly, the transition is a step discontinuity on a microstrip line as it is shown in Figure 1, where a microstrip line with a characteristics impedance Z0=50 Ohm, connected to another microstrip line of impedance Z1 and length L1. The transition is responsible to realize the impedance matching between the impedance of the feeding line and the SIW impedance. So, to make a good compromise between impedances, the geometrical parameters W2 and L2 must be suitably chosen to get a lower return loss. Figure 2 shows a schematic of impedance matching considered in this work [8]. By using small reflection approximation, we can compute the total reflection Gamma seen by the feed line (Equation 1). We note that we ignore the mismatch at the end of the microstrip line where the quasi-TEM mode in the microstrip couples to the TE10 of the SIW [9].

$$\Gamma = \Gamma_0 + \Gamma_1 e^{-2j\theta} \tag{1}$$

where;
$$\Gamma_0 = \frac{Z_1 - Z_0}{Z_1 + Z_0}$$
, $\Gamma_1 = \frac{Z_{siw} - Z_1}{Z_{siw} + Z_1}$, $\theta = \frac{2\pi}{\lambda_g} L_1$

In the TE_{10} propagation mode frequency region of interest, the SIW is equivalent to a conventional rectangular waveguide filled by a dielectric, hence, for the TE_{10} mode, the cross section impedance can be written as follows

$$Z_{siw} = \frac{2h}{W_{eff}} \frac{\eta_0}{\sqrt{\varepsilon_r}} \left[1 - (\frac{f_c}{f})^2 \right]^{-\frac{1}{2}}$$
(2)

We know that maximum power transfer from the input matching network to the waveguide structure will occur when:

$$\frac{\partial^2 \Gamma(Z_1, L_1)}{\partial Z_1 \partial L_1} = 0 \tag{3}$$

Solutions of (3) are:

$$\begin{cases} L_1 = (k+1)\frac{\lambda_g}{2} \\ and \\ Z_1 = \frac{Z_{siw}\sqrt{Z_o} - Z_o\sqrt{Z_{siw}}}{\sqrt{Z_{siw}} - \sqrt{Z_o}} \end{cases}$$
(4)

The design of the presented transition is split into two steps; in A band width of nearly 2.5 GHz is achieved the first step, an empty substrate integrated waveguide is designed, its impedance is calculated using the closed Equation (2). A 50 Ω microstrip line width is also calculated in this step. In the second step, Equation (4) allows to calculate Z₁, then the width W₂ is determined [9]. Later, Equation 2-b gives the length L which corresponds to the minimum return loss. An additional step (if necessary), is to use a full-wave simulator as HFSS in order to refine the values of Z₁ and L₁.

$$Z_1 = \frac{120\pi}{\sqrt{\varepsilon_{eff}} \left(\left(\frac{w_1}{h}\right) + 1.393 + 0.667 \cdot \ln(\frac{w_1}{h} + 1.444) \right)}$$
(5)

3 Simulation Results

Table 1 recapitulates values of three SIW structures versus frequency, geometrical parameters are calculated using the presented procedure and compared with values obtained using HFSS software. We can see that this method gives a quick solution and, at least an initial optimum point for sizing the transition. These obtained calculations of dimensions $(L \times W)$ can be taken into account as a first optimization. Simulation software can quickly obtain better optimization.

Parameters	Optimum value using HFSS		Calculated parameters		Relative error %	
	W_{I}	L_l	W ₁	L_l	Wı	L_l
f ₀ =10 GHz, εr=4.4	1.8121	3.9105	1.98510	3.5730	8.7149	8.6306
f ₀ =14.5 GHz, εr=2.2	2.4152	3.8221	2.2214	3.3368	8.0242	12.6972
f ₀ =21 GHz, εr=3.55	1.3945	2.1572	1.2204	1.8082	12.4848	16.1784

 TABLE I.
 GEOMETRICAL PARAMETER VALUES IN MILLIMITER.

The presented design procedure has been used in order to design transition for a substrate integrated waveguide working at 10 GHz with FR4-epoxy substrate, ε_r =4.4. The impedance of the transition is Z₁=36.2307 Ω , which corresponds to a width of 1.9810 mm.



Fig. 3.Scattering parameters of SIW with deduced and HFSS optimized (L×W) transition dimension for f=10GHz.

Comparisons between the S parameters of the first structure for our calculated values $(L \times W)$ and those optimized by HFSS of the 3 different frequency values are presented in Figs. 3, 4 and 5, we can note:

1- A bandwidth of around 2.5 GHz is reached for the first range of frequency (Fig.3).

2- A good agreement is obtained between the calculated and optimized (L×W) of S parameters.

3- For high-frequency values our optimal values of $(L \times W)$ give a better result of S parameter than that given by the optimal values obtained with HFSS (Figs.5 and 6).



Fig. 4.Scattering parameters of SIW with deduced and HFSS optimized (L×W) transition dimension for f=14.5GHz.

Now we turn our attention to the directional coupler, where the design method presented below is used to set, this time, a microstrip-to-directional coupler transition. The directional coupler is a cruciform H-plane 3dB/90⁰ hybrid coupler that consists of a rectangular cavity connected to four input/output ports. Metallic vias are inserted within the crossing area in order to produce the desired directional properties [10-13], Figure 6 shows the structure of the considered coupler.



Fig. 5.Scattering parameters of SIW with deduced and HFSS optimized (L×W) transition dimension for f=21GHz.

In this section, we won't present the coupler design method, but we only focus on the design of transition using the method presented below. A directional coupler is a network of four impedances that can be used as a power divider or power combiner by using an even-odd mode analysis technique and symmetric across the mid-plane characteristic. The coupler can be decomposed to a simple circuit as presented in Figure 7. The input impedance at any port of the coupler can be expressed by Equation 5. In this case $Z_{siw} = Z_0$ [9].



Fig. 6.Cruciform H-plane 3dB/90° hybrid coupler

The design strategy is divided into two parts; the first part consists in optimizing the coupler without transition, where the geometrical parameters of the metallic via give the desired S parameters. In the second part; the transition will be added where the dimensions of this one

will be calculated by the method presented below. Note that the impedance of the load is the impedance of the equivalent waveguide.



Fig. 7.Circuit of the branch-line hybrid coupler.

A 3dB coupler operating on the X-band is designed. The substrate is Duroid 5870 with $\varepsilon_r = 2.33$ and h = 0.787 mm. the transition dimensions are optimized using HFSS simulator and using the proposed method. The optimized length and width are 6.63 mm and 4.76 mm, respectively. The calculated length and width are 5.2 mm and 4.2 mm, respectively. It can be seen that the calculated dimensions are close to the optimized dimensions. It should be noted that it is impossible to determine the correct dimensions using the presented method but it can be used to determine start values for optimization.

Figures 8 and 9 show the coupler S-parameters, we have a return loss lower than -35dB at the operating frequency.

According to Figure 8, a simple shift in the bandwidth is noticed, even the optimization of the HFSS is similar to our case for S_{11} and S_{13} , whereas for s_{12} and s_{14} , they are almost identical (Fig.9).



Fig. 8.Simulated S₁₁ and S₁₃ parameters of cruciform coupler with optimized and our computed (L×W).



Fig. 9.Simulated S₁₁ and S₁₃ parameters of cruciform coupler with optimized and our computed (L×W).

4 Conclusion

In this work, analytical equations for the design of a discontinuity step transition are presented. The low reflection approximation method is used in order to find the relationship between all impedances and reflection factors. The proposed equations have been applied to design several transitions at different frequencies. In all cases, a good return loss has been obtained. The design procedure was applied for a microstrip-to-directional coupler transition. It allows finding a good starting point for optimization.

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