# Optimized PID Controller for a Self-lift Positive Output Luo-Converter

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Abstract. Voltage lift method opens a suitable way to enhance characteristics of DC-DC converters Self-lift can be defined as a typical approach extensively utilized in developing a DC-DC converter for amplifying output voltage. The use of the self-lift approach in the DC-DC conversion technologies result in designing the positive output Luo power converters with a high voltage transfer gains in the mathematical progression, such approach open the way to securely increasing voltage transfer gain that is related to the DC/DC converters as well as enhancing specifications regarding DC-DC converters. These converters could be utilized in the industrial applications as well as the computer peripheral equipment, particularly with regard to the high-output-voltage projects. Self-lift Positive Output Luo Converter in addition to the PID controller, therefore designed in the presented work, has appropriate output voltage regulations, also there aren't undershoots or overshoots and has been very effective. Detailed and full analysis regarding circuit in addition to discrete in addition to optimized PID controllers with the ABC algorithms was conducted. Furthermore, extensive simulations have been conducted and the results have been specified. PID controller platform has been proved with the use of MATLAB/Simulink model.

**Keywords:** DC-DC Luo converter, Artificial Bee Colony (ABC), Self-lift positive output Luo Converter, PID controller.

# **1 INTRODUCTION**

In the past decades, the DC-DC converters have been significantly utilized in power electronics applications. Recently, the research development, as well as manufacture, was extensively applied. DC-DC converters are having some features when compared to linear power supplies. Also, they are comparable in their size, yet it has high efficiency as well as high power density[1].

Being considered as major selection regarding converters change the major converters are developed and newly advanced self-lift positive output Luo converter. Luo converters are showing a lot of features like a simple startup, extremely low inrush current, high voltage conversion ratio, small size, high power density, micropower conversion, as well as high output voltage with the smooth ripples[2]. Along with such benefits, the self-lift approach has utilized the Luo converters that result in designing converters with compact and high-efficient DC-DC converters, as well as permitting output voltage increase in the arithmetic progression one stage at a time[1,2,3]. A lot of control approaches were examined with regard to dc-dc converters and major robust tuning method has been optimized PID controller that is providing excellent dynamic performances regardless of supply as well as load changes [1].

The most comprehensive analysis indicates as well as the simulations regarding various topologies related to Luo converter with voltage lift approach. It was specified that this approach exceeds the effect regarding parasitic elements in the traditional dc-dc converters as well as a massive extent for increasing output voltage levels [2]. Six distinctive DC-DC converters specified that have been prepared as well as resolved through the self-lift approach. Also, the study indicated that DC-DC converters have been provided through such an approach have been distinctive from conventional methods as well as displaying more effective performance. Also, the study suggested their applications in industries as well as consumer engineering products [3]. The details specified of novel approaches to design and tune PID Controllers that are including PID software packages, patentability PID tuning principles as well as the commercial hardware modules [4].

The full design and calculations with respect to the voltage lift approach indicated a lot of developed Luo converters. It was verified that such an approach decreases voltage-transfer gains concerning the discontinuous and continuous mode of operation [5].

The series SEPIC Converter with the voltage lift approach was specified. The study indicated that the SEPIC converter consequently developed with the use of such an approach has more advantages in many parts including high voltage transfer gain, easy circuit designs with no transformers or cascade connections [6]. Set up an automatic tuning key approach about the proportioned send-on-delta event- constructed PI controller. It might be indicated that the developed approach is providing proof regarding the high performance to measurement noise as well as initial PI tuning [7].

The tuning approach specified based on Ziegler–Nichols technique has been suggested, special structure regarding the proportional resonant controller. Also, it was indicated that the tuning approach offered suitable results as well as the performance with regard to fairly all the conditions, for reference frequencies below the final frequency [8].

The self-lift approach stated combining with the maximum power point tracking (MPPT) controllers as well as showing indications regarding effective tracking that is related to the required output voltage [9]. An efficient method introduced to build a representation for DC-DC buck-boost converters by MATLAB / software. Tuning progression of PID controllers created on identification circuit data used to obtain PID parameters to overcome the complexity and nonlinearity of the load voltage control mode [10].

It is obvious that a robust controller is needed to verify the performance characteristic of the control system in the power electronics circuit. The control goal is to achieve a closely constant output load voltage of a self lift positive output Luo-converter. However, the output voltage of non-minimum phase systems is complicated in the design process. The major goal of this study is to develop and examine the ABC-PID controller for the proposed converter to increase the robustness of the system.

## 2 MATHEMATICAL MODELING AND CIRCUIT ANALYSIS

Each self lift converters are originated from expanded converters for example Luo converters[11]. A self-lift positive output Luo-Converter and its circuits equivalent throughout the switch-on and switch off cycles are shown in **Figure 1** [11]. It is considered that the converter works in continuous conduction mode. The converter involves two inductors Lo and L, three capacitors Co, C1, and C, two diodes D1, D, and a semiconductor switch S. [12].



Fig. 1. (a) Self-lift positive output Luo-Converter circuit equivalent (b) Mode 1 operation(switch-on) (c) Mode 2 operation (switch off).

V1, and Vo denote the input voltage and output load voltage. Along with the self-lift circuit regarding P/O Luo-converters. Also, it has been obtained from the elementary circuit regarding P/O Luo-converters. Throughout the period of the switch-on, S, as well as D1, have been switched on, also D has been switched off. Throughout the period of the switch-off, D can be on, also S as well as D1 can be off [11].

With regard to the steady-state, the average regarding the inductor voltage values over the period has been equal to 0. Therefore,

Throughout the switch-on period, the voltage over the capacitor C1 has been equal to the voltage of the source. Assuming that C, as well as C1, have been large,

$$VC1=VI.$$
 (2)

Inductor current iL increase in the period of the switch-on as well as a decrease in the switch-off period. Furthermore, the equivalent voltages across the L have been VI in addition to - (VC–VC1). Thus,

$$kTVI = (1 - k) T (VC - VC1).$$
 (3)

Therefore,

$$VO = (1/(1-k)) VI$$
 (4)

Voltage transfer gain in continuous conduction mode (CCM) has been as develops:

$$M = \frac{V_0}{V_I} = \frac{1}{1 - k}$$
(5)

Due to the fact that all components have been ideal, power loss related to circuit elements has been ignored. Thus, output power  $P_0$  has been equal to input power  $P_{IN}$ 

$$V_O I_O = V_I I_I. ag{6}$$

Therefore,

$$=\frac{1}{1-k}I_0\tag{7}$$

Capacitor  $C_0$  acting as a low-pass filter, therefore  $I_{L0} = I_0$ . The charge related to the capacitor C increase throughout switch-on as well as decrease throughout switch-off:

$$Q_{+} = I_{C-on}kT = I_{0}kT$$

$$Q_{-} = I_{C-off}(1-k)T = I_{L}(1-k)T.$$
(8)
(9)

With regard to the switching period,  $Q_+ = Q_-$  and  $I_L = \frac{k}{1-k}I_0$ . Throughout the switch-off period,  $i_D = i_L + i_{LO}$ . Thus,

$$= I_L + I_{LO} = \frac{1}{1-k} I_O \tag{10}$$

The resulting are the characteristic equations of a self-lift positive output Luo-Converter [11]. Current variations:

$$\zeta = \frac{k}{2M^2} \frac{R}{F_s L_{eq}} \tag{11}$$

In which L<sub>eq</sub> indicating

 $I_I$ 

 $I_D$ 

$$L_{eq} = \frac{LL_0}{(L+L_0)} \tag{12}$$

Voltage variations:  

$$\varepsilon = \frac{k}{8M} \frac{1}{F_s^2 L_0 C_0}$$
(13)

The parameters of the proposed self-lift positive output Luo converter are listed in table1.

Table 1. Converter circuit parameter

Parameter	Symbol	Value
Input inductor	L	$1.5 * 10^{-5}$
Output inductor	Lo	$1.5 * 10^{-5}$
Capacitor	С	$2.1 * 10^{-5}$
Capacitor	C1	$2.5 * 10^{-5}$
Capacitor	Со	$2.5 * 10^{-5}$

Power output	Ро	100 W
Input voltage	V1	20 V
Switching frequency	Fs	50 <i>KHz</i>
Power switch	S	MOSFET
Resistor	R	25 Ω

## **3 CONTROL APPROACH**

Control plays a central role in automation and complex industrial processes. The PID control is intended to confirm the requiring requested nominal operating point for self-lift positive output Luo converter. Consider PID controller law regarding form [13].

$$u(t) = K_p * e(t) + K_i \int e(t) + K_d \frac{d e(t)}{dt}$$
(14)

Where:

u(t); control signal output.

e(t); Error signal.

k<sub>p</sub>; Proportional factor.

k<sub>i</sub>; Integral factor.

K<sub>d</sub>; Derivative factor.

Self-lift positive output Luo-Converter modeled using power electronics switch (MOSFETs) that cannot be linearized for linear PID controller design. PID controller has been a majorly benefited controller in a lot of the engineering control systems. Yet, adequately tuning the PID controller isn't simple even though it has at most 3 parameters. The difficulty especially coming from certain conflict requirements regarding the control system performance, also partly because of the complicated effect of PID parameters on the control performance. The important point to consider in the PID controller design for self-lift positive output Luo-Converter is how to calculate controller coefficients (kp , ki, kd). In order to locate the optimal PID controller parameters. Artificial bee colony ABC is used to design PID controller coefficients in the proposed work. The arrangement of the PID controller with ABC algorithms is presented in **Figure 2.** 



Fig. 2. The arrangement of the PID controller with ABC algorithms.

# 4 PID CONTROLLER DESIGN BASED ON ARTIFICIAL BEE COLONY ALGORITHM

The artificial bee colony (ABC) algorithm can be considered as one of the swarms established meta-heuristic algorithms which were developed to solve multimodal and multidimensional optimization problems, this algorithm is fairly new associated with regard to the swarm intelligence approaches. It has been suggested via Karaboga [14], this algorithm involves 3 bees' collections (employed bees, onlooker bees, scouts). Each collection has a special assignment in optimization development. The employed bees abuse the food resources and transmit the data around food resources back to the hive. They distribute such data with onlooker bees through boogying in elected boogie zone interior the hive. The nature of boogie is relational to the nectar gratified of food resource just abused by the boogying bee. The onlooker bees are remaining in the hive for the data and watching the boogie of the employed bees inside the hive, to choose a food resource. Therefore, suitable food resources invite supplementary onlooker bees evaluated to naughty ones. Whenever a food resource is used completely, all the employed bees related to it recklessness the food resource, and become a scout. Scout bees investigate for novel food resources. Employed and onlooker bees can be seen as an accomplishment of the work of manipulation, whereas scout bees can be seen as an accomplishment of the work of search [14][15].

For load voltage control of the self-lift positive output, Luo converter has been modeled in MATLAB/SIMULINK. The output load voltage regarding the converter is observed as the nectar amount. The duty cycle (D) of the self-lift positive output Luo-Converter is the decision variable that is characterized as a food resource position in the ABC algorithm [15]. The ABC algorithms are applied to search its globally optimal gains (kp, ki, kd) for the PID controller. The cost function of the Absolute time error method is given by the following [13][17][18]:

$$ISE = \int_0^{tsim.} e^2(t)dt \tag{15}$$

$$IAE = \int_0^{tsim.} |e(t)|dt \tag{16}$$

The optimization program is employed by employing a MATLAB m-file program and connected with the system model program in MATLAB/SIMULINK[19].

Flowchart of the intended ABC algorithm can be seen in Figure 3.



Fig. 3. The flowchart of ABC-PID control system

# **5 SIMULATION AND RESULTS**

To prove the ability and robustness regarding the suggested controller design, the response of the self-lift positive output Luo-Converter Converter is tested under numerous kinds of operations. The representation that is related to the closed-loop control system of self-lift positive output Luo-Converter model in MATLAB software can be seen in **Figure 4**.



Fig. 4. self-lift positive output Luo-Converter with closed-loop controller simulation diagram.

### 5.1 Start-up condition

**Figure 5.** depicts the simulation waveforms related to output voltages of Luo converter with the closed-loop converter in the time domain and take into consideration the light and heavy condition of the reference output voltages as (40 V, 60V and 80V). There isn't overshoot in the transient response regarding all start-up conditions. From the simulation results of this condition, it might be indicated that output voltage has a quick rise time (4 msec) for 40V (light condition). Also, the rise time of the output voltage response is closed to (6 msec) without overshoot for 80V (heavy condition).



**Fig. 5.** output load voltage startup (80 v,60v,40v), Start-up condition of the proposed system for load voltages (80 v,60v,40v).

#### 5.2 Reference tracking condition

The desired output voltage good tracks its reference signals under system uncertainties with an acceptable response as can be seen in **Figure 6**.



Fig. 6. output load voltage in reference tracking condition (40v,50v,60v) at t=0.015s, t=0.03s

## 5.3 Supply disturbance condition

To improve the influence of the controller, the converter response examined of disturbance of the sudden change in the supply voltage from 25V to 28V then 25V, while keeping the output voltage response at 40V is shown in **Figure 7**.



Fig. 7. Output load voltage in Supply disturbance condition (25v,28v,25v) at t=0.015s and t=0.03s.

#### 5.4 Load disturbance condition:

**Figure 8.** shown the load disturbance condition by the sudden change in the load from  $25\Omega$  to  $20\Omega$  then  $25\Omega$ . It is clear that the converter response shown a small change in the rise time and settling time, also without overshoot.



Fig. 8. output load voltage in Load disturbance condition  $(25\Omega, 20\Omega, 25\Omega)$  at t=0.015s and t=0.04s

#### 5.5 Time response characteristics:

Table 2. showing the results related to the time response characteristic of the Luo converter with PID-ABC controller design with two types of cost function as PID-ABC-IAE and PID-ABC-ISE. The results confirm that the controller gives a smooth response without overshoot and no steady-state error. Using the PID-ABC controller is observed by PID-ABC-IAE that the rise time 2.11 (msec) and settling time 6.5 (msec), but PID-ABC controller is observed by PID-ABC-ISE that the rise time 1.3 (msec) and settling time 3.5 (msec). The performance index PID-ABC-ISE has advantages like less settling time and fast rise time.

parameters	PID-ABC-IAE	PID-ABC-ISE
Rise time(s)	0.002112	0.0013223
Settling time(s)	0.006555	0.003537
Overshoot (%)	0	0
Steady state error	0	0
Closed loop stability	Stable	stable

Table 2. Result of time characteristics

The simulation results have been provided for verifying the performance and robustness of the optimized PID controller with regard to self-lift positive output Luo-converter. The controller in the system shown high robustness to the reference tracking condition and supply disturbance condition than load disturbance condition, furthermore the start-up condition.

#### 6 CONCLUSIONS

The article effectively demonstrates the modeling and simulation of the self-lift positive output Luo-Converter as well as the implementations regarding the PID-ABC controller and tested under different conditions by MATLAB\_SIMULINK.

The presented work showing that the settling time and rise time regarding the PID-ABC controller have been less in comparison to its analog matching part. It has been indicated that the converter with a PID controller designed on the basis of the ABC algorithm has been powerful, greater efficiency calms down fast with no steady-state. No more undershoots and overshoots, also the converter in addition to the controller has been qualified for tracking the needed references in spite of line as well as load changes. Therefore, it is suitable for several

stable power supplier real-world industrial usages and it is essentially intended for power supply in special medical devices and military applications, especially for high voltage transfer gain requests particularly for high output voltage developments.

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