On a Low-Complexity Subblock Partitioning Sequence for PAPR Reduction of OFDM System

 Po-Hao Chang, Shiann-Shiun Jeng, Jia-Ming Chen, and Jia-Yu Lin Department of Electrical Engineering, National Dong Hwa University
 No. 1, Sec.2, Da-Hsueh Rd., Shou-Feng, Hualien, Taiwan, R.O.C.
 E-mail address: po@mail.ndhu.edu.tw

Abstract-Conventional partial transmit sequence (PTS) orthogonal frequency division multiplexing (OFDM) system requires high hardware or high algorithm computational complexity to reduce peak to average power ratio (PAPR) so that signal distortion and power can be reduced during transmission. The Lim's PTS OFDM system is applied to this paper in the beginning with the aim at reducing hardware complexity. Since the emphasis of this paper is placed on subblock partitioning method, we then employ linear subblock partitioning method and realize the multiplications and the additions of radix-2 inverse fast Fourier transform (IFFT) butterfly diagram with linear characterization. We find that the complementary cumulative distribution function (CCDF) curve of linear subblock partitioning is close to that of non-linear subblock partitioning, and therefore our method can yield the same performance with more hardware complexity reduction.

Keywords – Orthogonal Frequency Division Multiplexing (OFDM); Peak to Average Power Ratio (PAPR); Partial Transmit Sequence (PTS)

I. INTRODUCTION

OFDM has been standardized as the European digital audio broadcasting (DAB) as well as digital video broadcasting (DVB) schemes. It has also been proposed for the third-generation mobile radio standard in Europe. Recently, it was selected as the high performance local area network (HIPERLAN) transmission technique. In addition, network wireless personal access (WPAN) and multi-band-OFDM ultra wide band (MB-OFDM UWB) systems have been accepted and written into the standard of IEEE 802.15.3a. In application of mobile communication network, OFDM technique is incorporated with multi-carrier-code division multiple access (MC-CDMA) which is regarded as 4G system [1].

There are three kinds of fixed lines in broadband communication, including high-speed digital subscriber line (HDSL), very-high-speed digital line (VDSL) and asymmetric digital subscriber line (ADSL) [2]. Besides, OFDM can be used in amusement devices such as DAB [3-5], DVB and high definition television (HDTV) [6,7]. In wireless local area network (WLAN), OFDM is currently adopted in four kinds of standards, including HIPERLAN of

CHINACOM 2010, August 25-27, Beijing, China Copyright © 2011 ICST 973-963-9799-97-4 DOI 10.4108/chinacom.2010.13 European standard, IEEE 802.11a, 802.11e and 802.11g of the American standard [3].

In OFDM system, PAPR can not be avoided according to the research in the past. It is more difficult in recent years for wireless communication system to accommodate all transmission data due to its drastic increase, and we have to increase subcarriers to meet the demand. This in turn aggravates the PAPR problem. We would confer with improving PAPR problem and reducing hardware complexity in our research.

We employ PTS scheme to reduce PAPR. Nevertheless, conventional PTS scheme must consume many IFFT's which yield high complexity, so our research refers to the Lim's PTS scheme for improvement [8]. Though it enjoys low complexity with pseudo random subblock partitioning, we can reduce complexity even further with linear partitioning method, and its CCDF performance will only encounter acceptable degradation. We utilize radix-2 linear input butterfly diagram to compute IFFT device complexity, bearing in mind that IFFT device includes both multiplications and additions.

The remainder of this paper is organized as follows. Section II discusses the Lim's PTS OFDM scheme to reduce complexity with linear partitioning method. Section III focuses on IFFT device reduction for new PTS OFDM scheme. Section IV derives performance analysis, and Section V provides conclusion.

II. STRUCTURE OF THE CONVENTIONAL AND THE LIM'S PTS SCHEMES

A. Structure of the conventional PTS scheme [9]

At the transmitter, the serial input data first passes through the serial to parallel converter. Then the parallel signal is mapped with QPSK modulation to generate the data block X. It is further partitioned into M disjoint subblocks $X_m = [X_{m,0}, X_{m,1}, ..., X_{m,N-1}]^T, m = 1, 2, ..., M$, such that $\sum_{m=1}^{M} X_m = X$. In order to collocate the subblocks, there exist four known subblock partitioning methods of PTS technique including pseudo random, adjacent, interleaved and symmetrization. Pseudo random, adjacent and interleaved

partitioning are common methods. Pseudo random method possesses the best CCDF performance, so we make use of it for PTS. In addition, symmetrization is also among one of our research. The parallel data must be partitioned into N/M subcarriers with pseudo random method, so we have M symbols with every symbol being supplied with N-N/M subcarriers of zeros, and we need M pieces of IFFT. Each X_m is passed through IFFT to form x_m which performs multiplication in turn with b_m before summation. Note that there is a limited number $W = 2^n$ of phase factors, $\varphi_m \in [0, 2\pi), m = 1, 2, ..., M$, such that $b_m = \exp(j\varphi_m)$. The set of phase factors is denoted as a vector $b = [b_1, b_2, ..., b_M]^T$. The time domain signal is given by the linear combination

$$x'(b) = \sum_{m=1}^{M} b_m \cdot x_m, \qquad (1)$$

where $x'(b) = [x'_0(b), x'_1(b), ..., x'_{NL-1}(b)]^T$ which is obtained by taking an inverse discrete Fourier transformation (IDFT) of length NxL on X_m concatenated with (L-1)×N zeros. Minimization of PAPR is related to the minimization of

$$\max_{0 \le k \le NL-1} |x_k(b)|.$$
 (2)

So the optimal algorithm with weight factors has W^{M-1} variations to compare with each other and we may select the one with minimum PAPR for transmission. PAPR can be written as

$$PAPR = \frac{\max_{0 \le k \le NL-1} |x'_{k}(b)|^{2}}{E\left[|x'_{k}(b)|^{2} \right]},$$
(3)

where $E[\cdot]$ denotes expectation.

B. Structure of the Lim's PTS scheme [8]



Figure 1. Block diagram of the Lim's PTS scheme.

Hardware complexity in the Lim's PTS scheme can be reduced compared with the conventional PTS scheme, but the CCDF performance will become a bit worse. This is because the conventional PTS makes use of M pieces of IFFT to transmit data, which results in high hardware complexity in the transmitter.

As an input, $A_{data} = [A_0A_1 \cdots A_{N-1}]^T \cdot Q_1^n$ is the n stage IFFT, where $n = log_2N \cdot Q_1^n$ can be separated into two parts, denoted as Q_1^l and $Q_{l+1}^n \cdot A_{data}$ is first passed through Q_1^l to form \hat{a}_{data} which can be written as

$$\hat{a}_{data} = A_{data} \cdot Q_1^l. \tag{4}$$

 \tilde{B}_m is the subblock partitioning method, and the constant r_m^w is chosen from a given alphabet \mathbb{Z} which is usually $\mathbb{Z} = \{\pm 1\}$ or $\mathbb{Z} = \{\pm 1, \pm j\}$. The set of constants is denoted as a vector $r^w = [r_1^w r_2^w \cdots r_M^w], 1 \le w \le W$, where $W = |\mathbb{Z}|^{M^{-1}} \cdot \hat{a}_{data}$ makes use of \tilde{B}_m to generate $\hat{a}_1, \hat{a}_2, \dots, \hat{a}_M$. After passing through Q_{l+1}^n and collocating with r_m^w , they are summed up to $a^w = [a_0^w a_1^w \cdots a_{M-1}^w]^T$ which can be written as

$$a^{w} = \sum_{m=1}^{M} r_{m}^{w} Q_{l+1}^{n} \tilde{B}_{m} Q_{l}^{l} A_{data}$$

$$= \sum_{m=1}^{M} r_{m}^{w} Q_{l+1}^{n} \tilde{B}_{m} \hat{a}_{data}.$$
(5)

With $P^w = \sum_{m=1}^{M} r_m^w \tilde{B}_m$, it can be re-written as

$$a^{w} = Q_{l+1}^{n} P^{w} \hat{a}_{data}.$$
 (6)

We select the signal sequence with the minimum PAPR from $a^1 \sim a^W$ and denote it as $a^{\tilde{w}}$. PAPR is defined as

$$PAPR(a^{\bar{w}}) = \frac{Max \left|a^{w}\right|^{2}}{E\left[\left|a^{w}\right|^{2}\right]},$$
(7)

where $E[\cdot]$ denotes expectation.

III. IFFT DEVICE REDUCTION FOR NEW PTS SCHEME



Figure 2. Subblock partitioning of symmetrical interleaved method for the Lim's PTS scheme.



Figure 3. 32 point IFFT DIT symmetrical interleaved input for M=8.

Figure 2 shows our proposed PTS method, the symmetrical interleaved method of linear subblock partitioning with the best CCDF performance which is close to the performance of pseudo random scheme without oversampling under the Lim's PTS structure. We make use of linear subblock partitioning method to reduce IFFT device complexity. Figure 3 shows 32 point IFFT for M=8 in which non-zero input passes through every additions and multiplications [10].

A. Multiplication

We learn from Figure 3 which multiplication will be used and the corresponding complexity can be computed. We derive from back- to front-end, and put marks on IFFT. At first, we derive multiplication complexity for M=2 which can be written as

If
$$n-l=2$$
, $C_2 = 1+2 = \sum_{k=1}^{log_2(2 \times M)} 2^{k-1}$
If $n-l=3$, $C_3 = 2+2 \times C_2 = \frac{2^3}{2 \times M} + 2 \times C_2$
If $n-l=4$, $C_4 = 4+2 \times C_3 = \frac{2^4}{2 \times M} + 2 \times C_3$
If $n-l=5$, $C_5 = 8+2 \times C_4 = \frac{2^5}{2 \times M} + 2 \times C_4$
:

where C_{n-l} is the complexity of stage n-l of IFFT. Then we derive multiplication complexity for M=4 which can be written as

If
$$n-l=3$$
, $C_3 = 1+2+4 = \sum_{k=1}^{\log_2(2\times M)} 2^{k-1}$
If $n-l=4$, $C_4 = 2+2\times C_3 = \frac{2^4}{2\times M} + 2\times C_3$
If $n-l=5$, $C_5 = 4+2\times C_4 = \frac{2^5}{2\times M} + 2\times C_4$
If $n-l=6$, $C_6 = 8+2\times C_5 = \frac{2^6}{2\times M} + 2\times C_5$
:
(9)

where C_6 needs 64 point IFFT for derivation. Finally, we derive multiplication complexity for M=8 which can be written as

$$If \ n-l = 4, \ C_4 = 1+2+4+8 = \sum_{k=1}^{\log_2(2\times M)} 2^{k-1}$$

$$If \ n-l = 5, \ C_5 = 2+2\times C_4 = \frac{2^5}{2\times M} + 2\times C_4 \quad (10)$$

$$If \ n-l = 6, \ C_6 = 4+2\times C_5 = \frac{2^6}{2\times M} + 2\times C_5$$

$$If \ n-l = 7, \ C_7 = 8+2\times C_6 = \frac{2^7}{2\times M} + 2\times C_6$$

$$\cdot$$

where C_7 needs 128 point IFFT for derivation. In summary, the derivation of multiplication complexity from Equations (8-10) can be re-written as

$$\begin{cases} C_{z} = \sum_{k=1}^{\log_{2}(2 \times M)} 2^{k-1} , z = \log_{2}(2 \times M) \\ C_{z} = \frac{2^{z}}{2 \times M} + 2 \times C_{z-1} , z = \log_{2}(2 \times M) + 1, \log_{2}(2 \times M) + 2, ..., n-l \end{cases}$$
(11)

where C_z corresponds to both $z = log_2(2 \times M)$ and $z = log_2(2 \times M) + 1, log_2(2 \times M) + 2, ..., n-l$. Equation (11) is for only one subblock of IFFT, and we must compute the total multiplication complexity which is defined as

$$\frac{N}{2} \times l + \frac{N}{2^{n-l}} \times C_z \times M$$

$$= \frac{N}{2} \times l + \frac{2^n}{2^{n-l}} \times C_z \times M$$

$$= \frac{N}{2} \times l + 2^l \times C_z \times M \quad , if \ n-l \ge \log_2(2 \times M),$$

$$N$$

$$N$$

$$N$$

$$(12)$$

where $\frac{N}{2} \times l$ is the complexity of Q_1^l and

 $2^{l} \times C_{z} \times M$ is the complexity of Q_{l+1}^{n} . Figure 4 shows the flow chart of multiplication complexity computation.



Figure 4. Flow chart of complexity computation.

B. Addition

Addition complexity derivation is the same as multiplication complexity derivation, but additions are on the back-end of every stage. Two additions are used at each cross-point between two lines. At first, we derive addition complexity for M=2 which can be written as

If
$$n-l=2$$
, $C_2 = 4+4 = \sum_{k=1}^{log_2 M} 2^{k+1} + 2^2$
If $n-l=3$, $C_3 = 4+2 \times C_2 = \frac{2^3}{M} + 2 \times C_2$
If $n-l=4$, $C_4 = 8+2 \times C_3 = \frac{2^4}{M} + 2 \times C_3$
If $n-l=5$, $C_5 = 16+2 \times C_4 = \frac{2^5}{M} + 2 \times C_4$
:
(13)

where C_{n-l} is the complexity of stage n-l of IFFT. Then we derive addition complexity for M=4 which can be written as

If
$$n-l = 3$$
, $C_3 = 4+8+8 = \sum_{k=1}^{\log_2 M} 2^{k+1} + 2^3$
If $n-l = 4$, $C_4 = 4+2 \times C_3 = \frac{2^4}{M} + 2 \times C_3$
If $n-l = 5$, $C_5 = 8+2 \times C_4 = \frac{2^5}{M} + 2 \times C_4$
If $n-l = 6$, $C_6 = 16+2 \times C_5 = \frac{2^6}{M} + 2 \times C_5$
:
(14)

where C_6 needs 64 point IFFT for derivation. Finally, we

derive addition complexity for M=8 which can be written as

If
$$n-l = 4$$
, $C_4 = 4+8+16+16 = \sum_{k=1}^{\log_2 M} 2^{k+1} + 2^4$
If $n-l = 5$, $C_5 = 4+2 \times C_4 = \frac{2^5}{M} + 2 \times C_4$
If $n-l = 6$, $C_6 = 8+2 \times C_5 = \frac{2^6}{M} + 2 \times C_5$
If $n-l = 7$, $C_7 = 16+2 \times C_6 = \frac{2^7}{M} + 2 \times C_6$
:

where C_7 needs 128 point IFFT for derivation. In summary, the derivation of addition complexity from Equations (13-15) can be re-written as

$$\begin{cases} C_{z} = \sum_{k=1}^{\log_{2}M} 2^{k+1} + 2^{\log_{2}(2\times M)} = \sum_{k=1}^{\log_{2}M} 2^{k+1} + 2 \times M , z = \log_{2}(2\times M) \\ C_{z} = \frac{2^{z}}{M} + 2 \times C_{z-1} , z = \log_{2}(2\times M) + 1, \log_{2}(2\times M) + 2, ..., n - l \end{cases}$$
(16)

where C_z corresponds to both $z = log_2(2 \times M)$ and $z = log_2(2 \times M) + 1$, $log_2(2 \times M) + 2$, ..., n - l. Equation (16) is for only one subblock of IFFT, and we must compute the total addition complexity which is defined as

$$N \times l + \frac{N}{2^{n-l}} \times C_z \times M$$

$$= N \times l + \frac{2^n}{2^{n-l}} \times C_z \times M$$

$$= N \times l + 2^l \times C_z \times M \quad \text{,if } n-l \ge \log_2(2 \times M),$$

$$N = N \times l + 2^l \times C_z \times M \quad \text{,if } n-l \ge \log_2(2 \times M),$$

where $\frac{I_v}{2} \times l$ is the complexity of Q_1^l and $2^l \times C_z \times M$

is the complexity of Q_{l+1}^n . Figure 4 also shows the flow chart of addition complexity computation.

IV. PERFORMANCE ANALYSIS

This section introduces two performance analyses. The first includes computational complexity reduction ratio (CCRR) of the Lim's PTS and that of the new PTS OFDM scheme over the conventional PTS OFDM scheme. The second is CCDF comparison between the Lim's PTS and new PTS OFDM scheme.

A. CCRR

The CCRR of the Lim's PTS OFDM scheme over the conventional PTS OFDM scheme is defined as [8]

$$CCRR_{Lim's} = (1 - \frac{Complexity of Lim's PTS}{Complexity of conventional PTS}) \times 100(\%)$$

$$= \frac{M \times l - l}{M \times n} = \frac{l \times (M - 1)}{M \times n}$$

$$= (1 - \frac{1}{M}) \times \frac{l}{n} \times 100(\%)$$
(18)

The complexity of the Lim's PTS scheme is given by

$$N_{multiplication \ complexity} = \frac{N}{2} \times log_2 N + \frac{N}{2} \times (n-l) \times (M-1)$$

$$N_{addition \ complexity} = N \times log_2 N + N \times (n-l) \times (M-1)$$
(19)

The CCRR of the new PTS scheme over the conventional PTS OFDM scheme is defined as

$$CCRR_{new} = (1 - \frac{Complexity of new PTS}{Complexity of conventional PTS}) \times 100(\%)$$
 (20)

We make use of Equations (18) and (20) to compute the CCRR in multiplication and in addition, as shown in Tables I-IV. The same CCRR and the same good PAPR performance of the new PTS can be obtained even as the number of n-l is increased.

Table I. CCRR in multiplication of the Lim's PTS

n- <i>l</i>	CCRR in multiplication of the Lim's PTS (%)				
	N=512(n=9)		N=2048(n=11)		
	M=2	M=8	M=2	M=8	
4	27.8	48.6	31.8	55.7	
6	16.7	29.2	22.7	39.8	

Table II. CCRR in multiplication of the new PTS

n- <i>l</i>	CCRR in multiplication of the new PTS (%)				
	N=512(n=9)		N=2048(n=11)		
	M=2	M=8	M=2	M=8	
4	44.5	72.2	45.4	75	
6	44.5	72.2	45.4	75	

Table III. CCRR in addition of the Lim's PTS

n- <i>l</i>	CCRR in addition of the Lim's PTS (%)				
	N=512(n=9)		N=2048(n=11)		
	M=2	M=8	M=2	M=8	
4	27.8	48.6	31.8	55.7	
6	16.7	29.2	22.7	39.8	

Table IV. CCRR in addition of the new PTS

n- <i>l</i>	CCRR in addition of the new PTS (%)				
	N=512(n=9)		N=2048(n=11)		
	M=2	M=8	M=2	M=8	
4	38.9	62.5	40.9	67.1	
6	38.9	62.5	40.9	67.1	

B. CCDF of PTS

We compare the effects on CCDF with the Lim's PTS and new PTS for 2048 subcarriers and M=8 in Figure 5. We learn that as the number of n-*l* becomes larger, CCDF's of the Lim's PTS and of the new PTS are close to that of the conventional PTS, but new PTS has fixed CCDF and the same CCRR and it has good PAPR performance with so much complexity reduced.



Figure 5. Comparing the CCDF of the Lim's PTS OFDM scheme with subblock partitioning of pseudo random and the CCDF of the new PTS OFDM scheme with symmetrical interleaved methods for N=2048(1680(data)+368(zeros)), M=8, W=2, n-*l*=4 and 6, QPSK, with oversampling.

V. CONCLUSION

This paper begins with the Lim's PTS scheme which modifies the conventional PTS scheme in order to reduce the hardware complexity of IFFT with good CCDF performance. The Lim's PTS scheme aims at non-linear subblock partitioning. When the parameters are set with N=2048, M=8, n-l=4 and 6, we employ symmetrical interleaved subblock partitioning method to reduce hardware complexity of IFFT with good CCDF performance. Its performance has been analyzed in reference to the standard of IEEE 802.16 for wireless metropolitan area network (WMAN).

VI. REFERENCE

- "Universal mobile telecommunications system (UMTS); UMTS terrestrial radio access (UTRA); Concept evaluation," Tech. Rep., ETSI, TR 101 146, 1997.
- [2] John A. C. Bingham, ADSL, VDSL, and multicarrier modulation, January 2000, John Wiley & Sons Inc.
- [3] John Terry and Juha Heiskala, *OFDM wireless LANs: A theoretical and practical guide*, December 2001, Macmillan Computer Pub.
- [4] Radio broadcasting system: Digital Audio Broadcasting (DAB) to mobile, portable and fixed receivers. ETS 300 401, ETSI-European Telecommunication Standards Institute, Feb. 1995.
- [5] Wolfgang Hoeg and Thomas Lauterbach, Digital audio broadcasting: Principles and applications of digital radio, October 2003, John Wiley & Sons Inc.
- [6] Robert Goodman, The guide to digital television, second edition, April 1999, Miller Freeman PSN Inc.
- [7] Digital broadcasting systems for television, sound and data services. *European Telecommunication Standard*, prETS 300 744 (Draft, version 0.0.3), April 1996.
- [8] Dae-Woon Lim, Seok-Joong Heo, Jong-Seon No, and Habong Chung, "A Lim's PTS OFDM scheme with low complexity for PAPR reduction," *IEEE Trans. on Comm.*, vol. 52, pp. 77-82, March 2006.
- [9] Seung Hee Han and Jae Hong Lee, "An overview of peak-to-average power ratio reduction techniques for multicarrier transmission," *IEEE Trans. on Comm.*, vol. 12, pp. 56-65, April 2005.
- [10] S. He and M. Torkelson, "Designing pipeline FFT processor for OFDM (de)modulation," Proceeding of URSI Int. Symp. Signals, Systems, and Electronics. 1998.